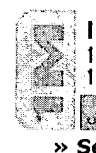


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**1 Low power FSM design using Huffman-style encoding**

Surti, P.; Chao, L.F.; Tyagi, A.;

European Design and Test Conference, 1997. ED&amp;TC 97. Proceedings, 17-20 March 1997

Pages:521 - 525

[\[Abstract\]](#)   [\[PDF Full-Text \(456KB\)\]](#)   IEEE CNF
**2 Controller power estimation using information from behavioral description**

Surti, P.; Chao, L.-F.;

Circuits and Systems, 1996. ISCAS '96., 'Connecting the World', 1996 IEEE International Symposium on ,Volume: 4, 12-15 May 1996

Pages:679 - 682 vol.4

[\[Abstract\]](#)   [\[PDF Full-Text \(336KB\)\]](#)   IEEE CNF
**3 Exploiting skewed state probabilities for low power state assignment**

Bhupathi, I.; Chao, L.-F.;

Circuits and Systems, 1996. ISCAS '96., 'Connecting the World', 1996 IEEE International Symposium on ,Volume: 4, 12-15 May 1996

Pages:759 - 762 vol.4

[\[Abstract\]](#)   [\[PDF Full-Text \(340KB\)\]](#)   IEEE CNF
**4 Efficient retiming and unfolding**

Chao, L.-F.; Sha, E.H.-M.;

Acoustics, Speech, and Signal Processing, 1993. ICASSP-93., 1993 IEEE International Conference on ,Volume: 1, 27-30 April 1993

Pages:421 - 424 vol.1

[\[Abstract\]](#)   [\[PDF Full-Text \(288KB\)\]](#)   IEEE CNF
**5 Static scheduling of uniform nested loops**

Chao, L.-F.; Sha, E.H.-M.;

Parallel Processing Symposium, 1993., Proceedings of Seventh International

16 April 1993  
Pages:254 - 258

[\[Abstract\]](#) [\[PDF Full-Text \(396KB\)\]](#) IEEE CNF

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**6 Rate-optimal static scheduling for DSP data-flow programs**

*Chao, L.-F.; Sha, E.H.-M.;*

VLSI, 1993. Design Automation of High Performance VLSI Systems, Proceedings of the Third Great Lakes Symposium on , 5-6 March 1993  
Pages:80 - 84

[\[Abstract\]](#) [\[PDF Full-Text \(404KB\)\]](#) IEEE CNF

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**7 Efficient distributed reconfiguration for binary trees on Diogenes machines**

*Sha, E.H.-M.; Chao, L.-F.;*

Computers and Communications, 1992. Conference Proceedings., Eleventh Annual International Phoenix Conference on , 1-3 April 1992  
Pages:464 - 471

[\[Abstract\]](#) [\[PDF Full-Text \(592KB\)\]](#) IEEE CNF

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**8 Algorithms for min-cut linear arrangements of outerplanar graphs**

*Chao, L.-F.; Sha, E.H.-M.;*

Circuits and Systems, 1992. ISCAS '92. Proceedings., 1992 IEEE International Symposium on , Volume: 4 , 3-6 May 1992  
Pages:1851 - 1854 vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(324KB\)\]](#) IEEE CNF

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**9 Unfolding and retiming data-flow DSP programs for RISC multiprocessor scheduling**

*Chao, L.-F.; Sha, E.H.-M.;*

Acoustics, Speech, and Signal Processing, 1992. ICASSP-92., 1992 IEEE International Conference on , Volume: 5 , 23-26 March 1992  
Pages:565 - 568 vol.5

[\[Abstract\]](#) [\[PDF Full-Text \(312KB\)\]](#) IEEE CNF

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**10 Design for easily applying test vectors to improve delay fault coverage**

*Sha, E.H.-M.; Chao, L.-F.;*

Computer-Aided Design, 1991. ICCAD-91. Digest of Technical Papers., 1991 International Conference on , 11-14 Nov. 1991  
Pages:500 - 503

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1 **Composite metal etching for submicron integrated circuits**

Riley, P.E.; Ben-tzur, M.; Kavari, R.;

Semiconductor Manufacturing, IEEE Transactions on , Volume: 8 , Issue: 3 , 1995

Pages:309 - 313

[\[Abstract\]](#) [\[PDF Full-Text \(360KB\)\]](#) IEEE JNL
2 **In-line defect density targets for new technology from development manufacturing**

Shamble, E.; Ben-Tzur, M.; Sharifzadeh, S.;

Advanced Semiconductor Manufacturing Conference and Workshop, 1998. 19 IEEE/SEMI , 23-25 Sept. 1998

Pages:171 - 173

[\[Abstract\]](#) [\[PDF Full-Text \(180KB\)\]](#) IEEE CNF
3 **A Low cost, microprocessor compatible, 18.4 um/sup 2/,6-t bulk ce technology for high speed SRAMS**

Helm, M.; Kavanaugh, W.; Liew, B-K.; Petti, C.; Stolmeijer, A.; Ben-tzur, M.; Bornstein, J.; Lilygren, J.; Ting, W.; Trammel, P.; Allan, J.; Gray, G.; Hartran Radigan, S.; Shanmugan, J.K.; Shrivastava, R.;

VLSI Technology, 1993. Digest of Technical Papers. 1993 Symposium on , Ma 19, 1993

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## 1 On Steiner tree problem with 45° routing

Lee, D.T.; Shen, C.-F.; Ding, C.-L.;

Circuits and Systems, 1995. ISCAS '95., 1995 IEEE International Symposium, Volume: 3, 28 April-3 May 1995

Pages:1680 - 1682 vol.3

[Abstract] [PDF Full-Text (228KB)] IEEE CNF

## 2 A new optimization driven clustering algorithm for large circuits

Ding, C.-L.; Ho, C.-Y.; Irwin, M.J.;

Design Automation Conference, 1993, with EURO-VHDL '93. Proceedings EUR DAC '93. European, 20-24 Sept. 1993

Pages:28 - 32

[Abstract] [PDF Full-Text (328KB)] IEEE CNF

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**Results Key:****JNL** = Journal or Magazine   **CNF** = Conference   **STD** = Standard**1 Optimal orientations of transistor chains***Her, T.W.; Wong, D.F.; Freeman, T.H.;*Computer-Aided Design, 1990. ICCAD-90. Digest of Technical Papers., 1990 )  
International Conference on , 11-15 Nov. 1990

Pages:524 - 527

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